

EAST - [400.wsp:1]

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☐ L1: (315460) multiplication\$1 or multiply or multiplying or multiplier\$1
☐ L2: (273976) multimedia multi adj media packed simd
☐ L3: (578) 1 near7 2
☐ L4: (183) instruction\$1 near5 3
☐ L5: (8899410) "8" or "16"
☐ L6: (4015556) "32" or "64"
☐ L7: (8926132) eight or sixteen or 5
☐ L8: (4018928) thirty adj two or sixty adj four or 6
☐ L9: (549635) 7 with 8
☐ L10: (8) 4 with 9
☐ L11: (27) 4 same 9 not 10
☐ L12: (148) 4 not (10 11)
☐ L13: (25) 1.ti. and 12
☐ L14: (0) 20030212727.URPN.
☐ L15: (36) 1.ab. and 12 not 13
☐ L17: (87) 12 not (13 15)
☐ L18: (22610) 708/.cccls.
☐ L19: (48) 17 and 18
☐ L20: (39) 17 not 19

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 Default operator: OR ☒ Highlight all hit terms initially

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C	P	2	3	In
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6711602 B1	20040323	42	Data processor with flexible multiplv unit	708/625			Bhandal, Amarjit Singh et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6370559 B1	20020409	25	Method and apparatus for performing N bit by 2*N-1 bit sio	708/625			Hoffman, Nathaniel	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	<input type="checkbox"/>	US 5675526 A	19971007	20	Processor performing packed data multiplication	708/620	708/518; 712/221		Peleg, Alexander et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6014684 A	20000111	25	Method and apparatus for performing N bit by 2*N-1 bit sio	708/620	708/625		Hoffman, Nathaniel	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5793661 A	19980811	46	Method and apparatus for performing multiplv and accumul	708/603			Dulong, Carole et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6574651 B1	20030603	24	Method and apparatus for arithmetic operation on vectored	708/523	708/520; 708/524		Cui, Jeffrey et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6282556 B1	20010828	38	High performance pipelined data path for a media process	708/521	708/201; 708/620		Chehrizi, Farzad et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
8	<input type="checkbox"/>	<input type="checkbox"/>	US 5862067 A	19990119	20	Method and apparatus for providing high numerical accura	708/501	708/490; 708/508		Mennemeier, Larry M. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6058408 A	20000502	29	Method and apparatus for multiplv and accumulatio co	708/322			Fischer, Stephen A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

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